

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,176,530 B1
APPLICATION NO. : 10/803203
DATED : February 13, 2007
INVENTOR(S) : Bulucea et al.

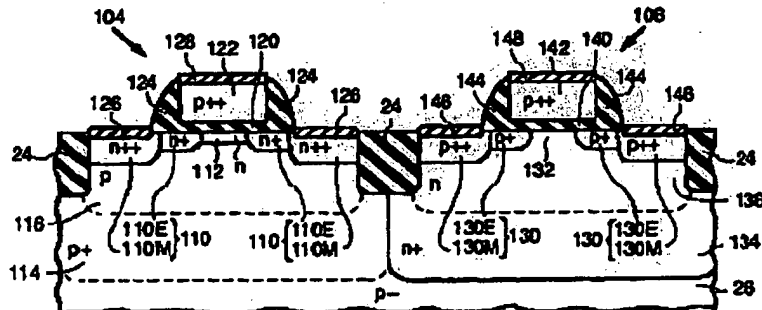
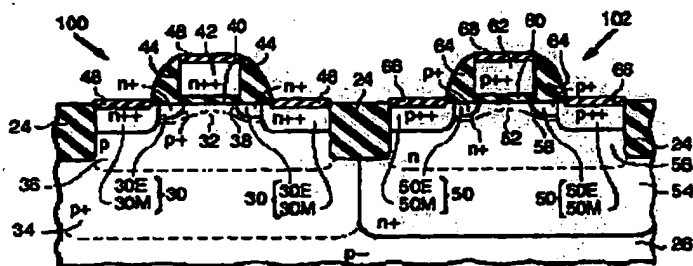
Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Delete the title page and substitute therefore the attached title page.

The title should read "SEMICONDUCTOR STRUCTURE HAVING N-CHANNEL CHANNEL-JUNCTION FIELD-EFFECT TRANSISTOR".

The drawing on the cover page should be:



This certificate supersedes
Certificate of Correction
Issued July 28, 2009.



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(12) **United States Patent**
Bulucea et al.

(10) **Patent No.:** **US 7,176,530 B1**

(45) **Date of Patent:** Feb. 13, 2007

- (54) SEMICONDUCTOR STRUCTURE HAVING
N-CHANNEL CHANNEL-JUNCTION
FIELD-EFFECT TRANSISTOR

6.916.698 B2 * 7/2005 Mocuta et al.: 438/217

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(US)

FOREIGN PATENT DOCUMENTS

EP 298421 A2 5/1988 27/10

- (73) Assignee: National Semiconductor Corporation,
Santa Clara, CA (US)

OTHER PUBLICATIONS

- (*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 11 days.

Appels et al., "Local Oxidation of Silicon and Its Applications in Semiconductor-Device Technology", *Philips Res. Rpts.*, vol. 25, 1970, pp. 118-132.

(Continued)

- (21) Appl. No.: 10/803,203

Primary Examiner—T. N. Ouach

- (22) Filed: Mar. 17, 2004

(74) *Attorney, Agent, or Firm*—Ronald J. Meetin

- (51) Int. Cl.
H01L 29/76 (2006.01)

- (57)
- ABSTRACT**

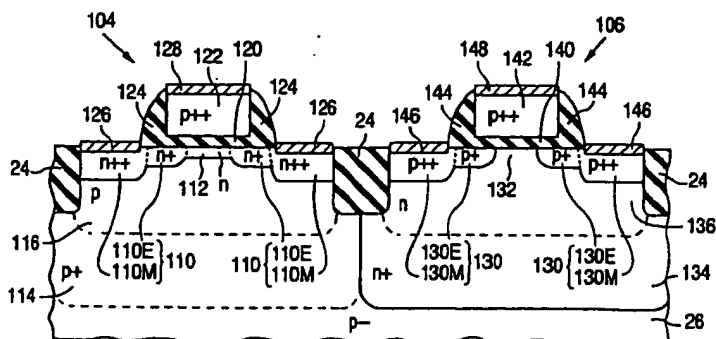
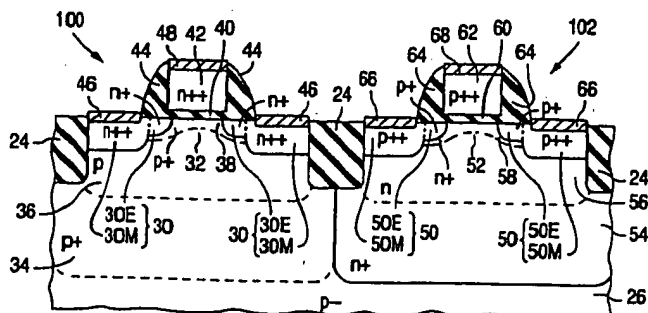
- (52) U.S. CL 257/369; 257/403

- (58) **Field of Classification Search** 257/288,
257/350, 351, 368, 369, 371, 403
See application file for complete search history.

- (56)
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66 Claims, 24 Drawing Sheets